	Application No.	Applicant(s)	
Notice of Allowability	09/324,905	BITTNER ET AL.	
	Examiner	Art Unit	
	Lilian Vo	2127	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31:	(OR REMAINS) CLOSED in the comment of the comment o	n this application. If not included unication will be mailed in due course. THIS	ive
1. This communication is responsive to <u>communication filed</u>	<u>on 12/22/04</u> .		
2. The allowed claim(s) is/are <u>1 - 23 and 26 - 39 now renumb</u>	<u>bered as 1 - 37</u> .		
3. \boxtimes The drawings filed on <u>02 June 1999</u> are accepted by the E	Examiner.		
4. Acknowledgment is made of a claim for foreign priority unall All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONITHIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submined in the priority of the priority of the priority of the complex of the priority of the complex of the priority of the complex of the priority of the prior	e been received. e been received in Application or process have been received. Tof this communication to file MENT of this application. Initted. Note the attached Expressive reason(s) why the oath of the submitted. To son's Patent Drawing Reviews and the submitted. To son's Patent Drawing Reviews and the submitted of the submitted. To son's Patent Drawing Reviews and the submitted of the submit	on No ed in this national stage application from the ed in this national stage application from the earned in this national stage application from the earned ear	
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB. Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Paper No /08), 7. ☒ Examiner	nformal Patent Application (PTO-152) Summary (PTO-413), Mail Date s Amendment/Comment s Statement of Reasons for Allowance LEWIS A. PLULOCK, IR. PRIMARY EXAMPLE	

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DETAILED ACTION

Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance:

The prior arts of record fail to teach or fairly suggest all of the limitations recited in the claims 1, 5, 7, 8, 11, 12, 15, 16, 20, 26, 33 and 37, specifically the use of extended registers for addressing the peripheral devices by generating the address signal which is concatenating the address extension from an address extension register with the processor address signal.

Bigbee et al. (US 6,526,431) discloses a method and apparatus for switching first and second tasks in an operating system with each have first and second traditional states and first and second extended states by saving the first traditional and extended states from the processing unit to a buffer, updating the buffer by replacing the first extended state by the second extended state, and restoring the first traditional state and the second extended state in the processing unit from the updated buffer.

Samson et al. (US 5,995,750) discloses a memory protection system in a multitasking system for preventing a currently running application from writing data to a memory segment storing data for a second application by transmitting m upper address defining a memory segment of the currently running application to the memory from a memory register, detecting when a currently running application generates an invalid address, and disabling the data operation to prevent the corruption of data in a memory segment storing data for the currently running application.

Therefore, since none of prior art of record fairly teaches the generating of a peripheral address signal/second address signal, the claims are allowed.

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2. Any comments considered necessary by applicant must be submitted no later than

the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Lilian Vo whose telephone number is 571-272-3774. The

examiner can normally be reached on Monday - Thursday, 7:30am - 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Meng-Ai An can be reached on 571-272-3756. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For

more information about the PAIR system, see http://pair-direct.uspto.gov. Should you

have questions on access to the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free).

Lilian Vo Examiner Art Unit 2127 LEWIS A. BULLOCK, JR.
ORIMARY FYANGER

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December 23, 2004